

SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1YAG91-02
Date	March 16, 2026

TYPE : TCG104VGLABANN-AN30-TA
<10.4 inch VGA transmissive color TFT with LED backlight
and constant current circuit for LED backlight>

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KYOCERA CORPORATION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: QA dept.
	Prepared	Checked	Approved	Approved
February 14, 2023	<i>N. Yamawaki</i>	<i>T. Masuda</i>	<i>I. Kawajiri</i>	<i>T. Sawada</i>

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Warning

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

Caution

1. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.
2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

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Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.
		Prepared	Checked	Approved	Approved
March 16, 2026		<i>N. Yamawaki</i>	<i>T. Masuda</i>	<i>I. Kawajiri</i>	<i>T. Sawada</i>
Rev. No.	Date	Page	Descriptions		
01	Apr. 26, 2023	3	5-1. LCD Permissive input ripple voltage: Correct the maximum value and unit.		
02	Mar. 16, 2026	-	Removed "CONFIDENTIAL" notation		
		2	4-2. Environmental absolute maximum ratings Revised note on operating temperature		
		2-7	Changed symbols Temp. → Ta Ta → Temp.		
		8	7. Interface signals -Revised description for CK and ENAB -Added "CN1" designation to LCD connector description		
		10, 11	8. Input timing characteristics Changed item names for terminology consistency in the specification and added symbols		
		12	9. Lot number identification Revised the description		

1. Application

This document defines the specification of TCG104VGLABANN-AN30-TA. (RoHS Compliant)

2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Anti-Glare treatment
Additional circuit	: Timing controller, Power supply (3.3V input) With constant current circuit for LED Backlight (12V)

3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	240.7(W)×(180.2)(H)×9(D)	mm
Active area	211.2(W)×158.4(H) (26.4cm/10.4 inch(Diagonal))	mm
Dot format	640×(R,G,B)(W)×480(H)	dot
Dot pitch	0.11(W)×0.33(H)	mm
Base color 2)	Normally White	-
Mass	470	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

4. Absolute maximum ratings

4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)		V _{DD}	0	4.0	V
Supply voltage(+12V)		V _{IN}	-0.3	14.0	V
Input signal voltage	1)	V _{I1}	-0.3	6.0	V
	2)	V _{I2}	-0.3	V _{IN}	V

1) Input signal : CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

2) Input signal : BLBRT, BLEN

4-2. Environmental absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Operating temperature	1)	T _{OP}	-20	70	°C
Storage temperature	2)	T _{STO}	-30	80	°C
Operating humidity	3)	H _{OP}	10	4)	%RH
Storage humidity	3)	H _{STO}	10	4)	%RH
Vibration		-	5)	5)	-
Shock		-	6)	6)	-

1) Operating temperature range defines the operation only and the contrast, response time and other display optical characteristics are set at Ta (Ambient Temperature) =25°C.

2) Ta=-30°C<48h, Ta=80°C<168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock.

An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to "Precautions for Use" for details.)

3) Non-condensing

4) Ta≤40°C, 85%RH Max.

Ta>40°C, Absolute humidity shall be less than 85%RH at 40°C.

5)

Frequency	10~55 Hz	Acceleration value (0.3~9 m/s ²)
Vibration width	0.15mm	
Interval	10-55-10 Hz	1 minutes

2 hours in each direction X, Y, Z (6 hours total)

EIAJ ED-2531

6) Acceleration: 490 m/s², Pulse width: 11 ms

3 times in each direction: ±X, ±Y, ±Z

EIAJ ED-2531

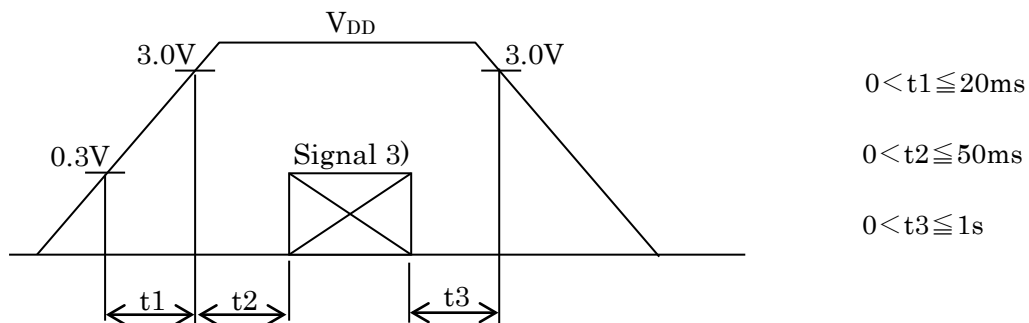
5. Electrical characteristics

5-1. LCD

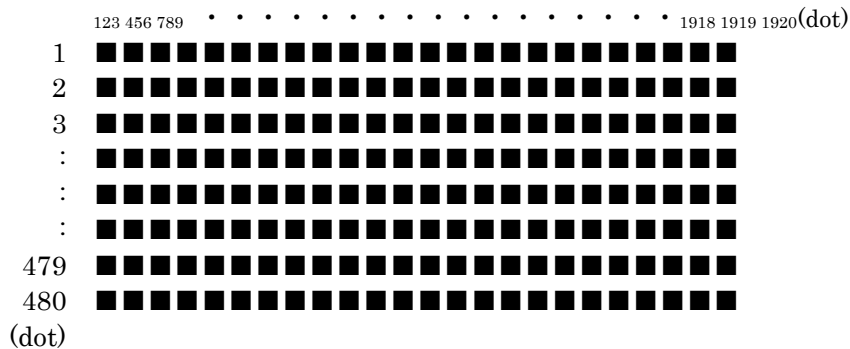
 $T_a = -20 \sim 70^\circ\text{C}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage 1)	V_{DD}	-	3.0	3.3	3.6	V
Current consumption	I_{DD}	2)	—	150	185	mA
Permissive input ripple voltage	V_{RP}	-	-	—	100	mV _{p-p}
Input signal voltage 3)	V_{IL}	"Low" level	0	—	0.8	V
	V_{IH}	"High" level	2.0	—	V_{DD}	V

1) V_{DD} -turn-on conditions



2) Display pattern:

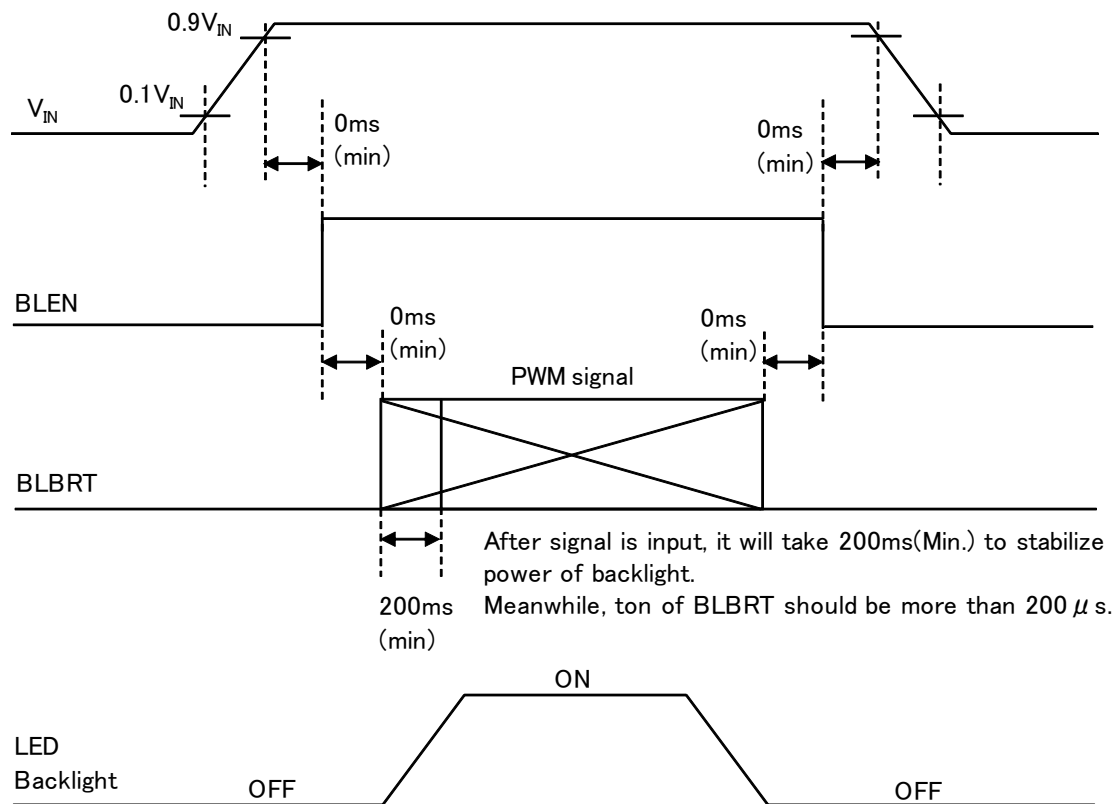
 $V_{DD}=3.3\text{V}$, $T_a=25^\circ\text{C}$


3) Input signal : CK, R0~R5, G0~G5, B0~B5, H_{SYNC}, V_{SYNC}, ENAB, R/L, U/D

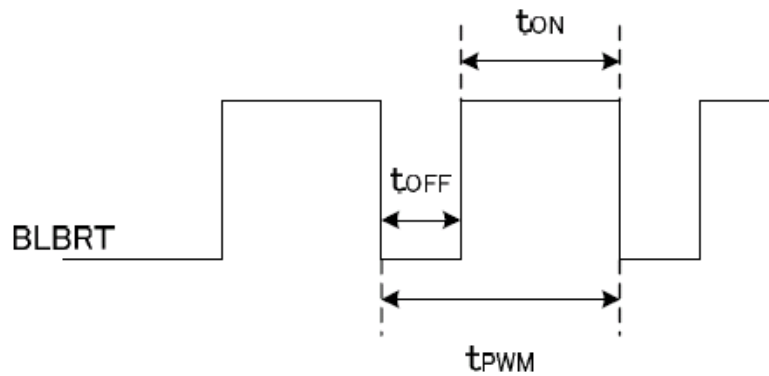
5-2. Constant current circuit for LED backlight

 $T_a = -20 \sim 70^\circ\text{C}$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage 1)	V_{IN}	-	10.8	12.0	13.2	V
Current consumption	I_{IN}	2)	-	375	490	mA
Permissive input ripple voltage	V_{RP_BL}	$V_{IN}=12.0\text{V}$	-	-	100	mVp-p
BLBRT Input signal voltage	V_{IL_BLBRT}	"Low" level	0	-	0.8	V
	V_{IH_BLBRT}	"High" level	2.3	-	V_{IN}	V
BLBRT Input pull-down resistance	R_{IN_BLBRT}	-	100	300	500	k Ω
BLEN Input signal voltage	V_{IL_BLEN}	"Low" level	0	-	0.8	V
	V_{IH_BLEN}	"High" level	2.3	-	V_{IN}	V
BLEN Input pull-down resistance	R_{IN_BLEN}	-	100	300	500	k Ω
PWM Frequency 3)	f_{PWM}	-	200	-	10k	Hz
PWM Duty ratio 3)	D_{PWM}	$f_{PWM}=200\text{Hz}$	1	-	100	%
		$f_{PWM}=2\text{kHz}$	10	-	100	%
		$f_{PWM}=10\text{kHz}$	50	-	100	%
Operating life time 4), 5)	T	Temp.=25°C	-	100,000	-	h

1) V_{IN} -turn-on conditions2) $V_{IN}=12\text{V}$, $T_a=25^\circ\text{C}$, $D_{PWM}=100\%$

3) PWM timing diagram



Please do not set t_{OFF} with $0 \mu s < t_{OFF} < 1 \mu s$.

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

4) When brightness decrease 50% of minimum brightness.

The average life of a LED will decrease when the LCD is operating at higher temperatures.

5) Life time is estimated data. (Condition: $D_{PWM}=100\%$, Temp.= 25°C in chamber).

6. Optical characteristics

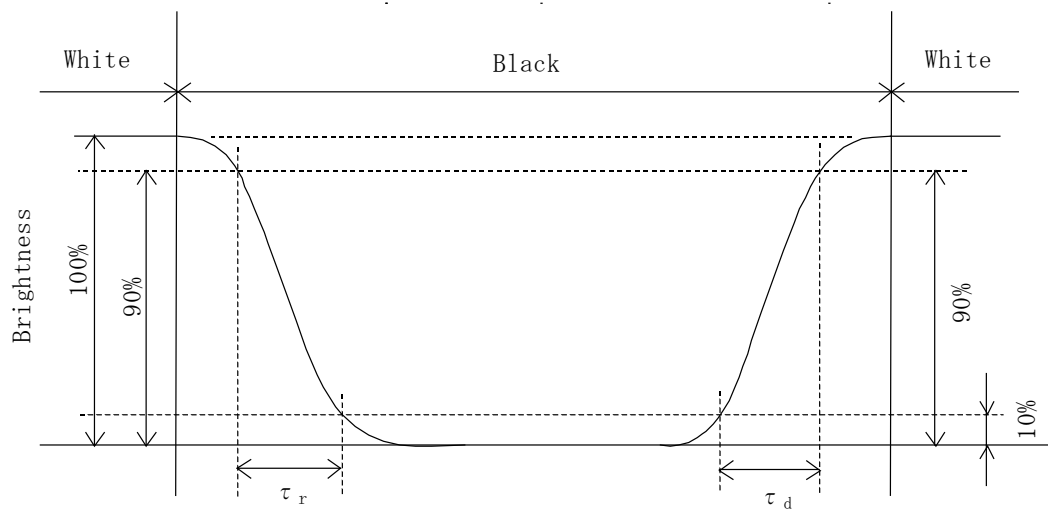
Measuring spot= ϕ 6.0mm, Ta=25°C

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Response time	Rise	τ_{r}	$\theta = \phi = 0^{\circ}$	—	10	—	ms
	Down	τ_{d}	$\theta = \phi = 0^{\circ}$	—	20	—	ms
Viewing angle range View direction : 6 o'clock (Gray inversion)		θ_{UPPER}	$\text{CR} \geq 10$	—	60	—	deg
		θ_{LOWER}		—	70	—	
		ϕ_{LEFT}		—	70	—	deg
		ϕ_{RIGHT}		—	70	—	
Contrast ratio		CR	$\theta = \phi = 0^{\circ}$	350	500	—	—
Brightness		L	D _{PWM} =100%	560	800	—	cd/m ²
Chromaticity coordinates	Red	x	$\theta = \phi = 0^{\circ}$	0.555	0.605	0.655	—
		y		0.285	0.335	0.385	
	Green	x	$\theta = \phi = 0^{\circ}$	0.290	0.340	0.390	
		y		0.490	0.540	0.590	
	Blue	x	$\theta = \phi = 0^{\circ}$	0.105	0.155	0.205	
		y		0.055	0.105	0.155	
	White	x	$\theta = \phi = 0^{\circ}$	0.255	0.305	0.355	
		y		0.265	0.315	0.365	

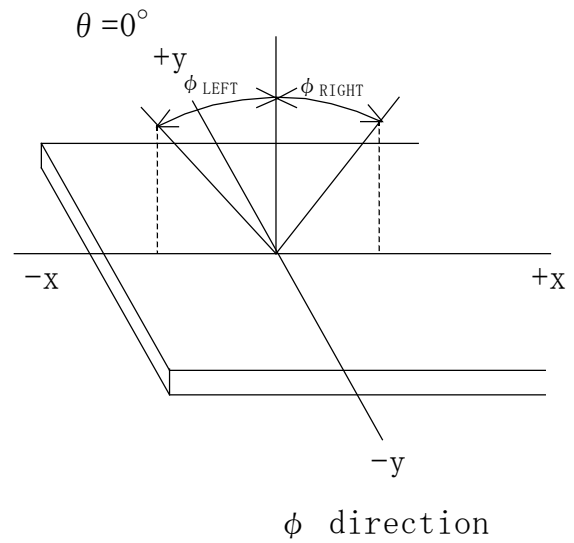
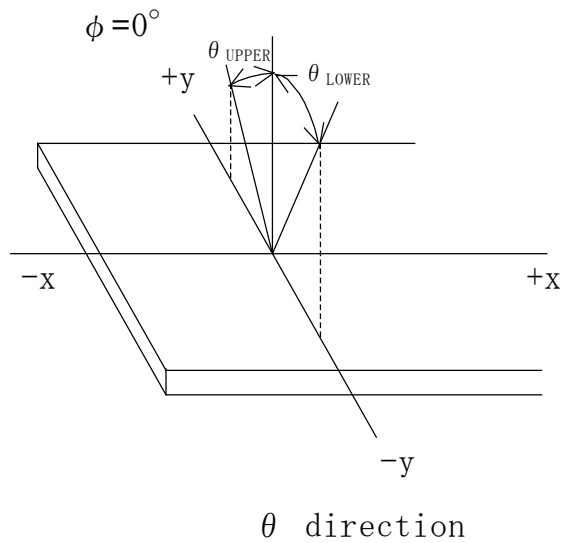
6-1. Definition of contrast ratio

$$CR(\text{Contrast ratio}) = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

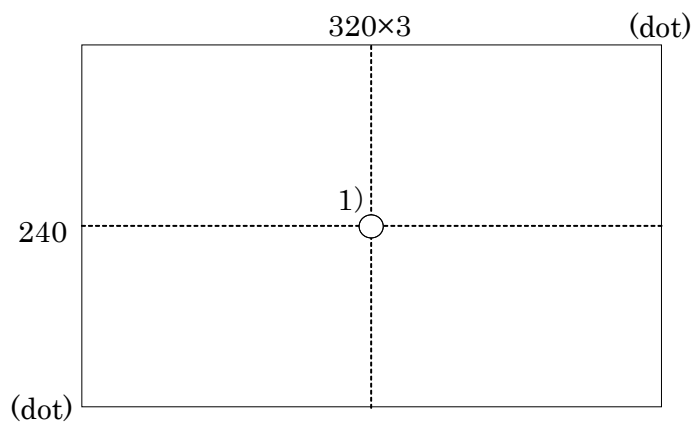
6-2. Definition of response time



6-3. Definition of viewing angle



6-4. Brightness measuring points



- 1) Rating is defined as the white brightness at center of display screen.
- 2) 5 minutes after LED is turned on. ($T_a=25^\circ\text{C}$)

7. Interface signals

No.	Symbol	Description	I/O	Note
1	GND	GND	-	
2	GND	GND	-	
3	GND	GND	-	
4	BLBRT	PWM signal (Brightness adjustment)	-	
5	BLBN	ON/OFF terminal voltage	-	
6	V _{IN}	+12V power supply	-	
7	V _{IN}	+12V power supply	-	
8	V _{IN}	+12V power supply	-	
9	NC	No connect	-	
10	U/D	Vertical display mode select signal H: Normal, L: Up / Down reverse mode	I	2)
11	R/L	Horizontal display mode select signal L: Normal, H: Left / Right reverse mode	I	2)
12	V _{DD}	3.3V power supply	-	
13	V _{DD}	3.3V power supply	-	
14	ENAB	Data Enable signal (positive)	I	1)
15	GND	GND	-	
16	B5	BLUE data signal (MSB)	I	
17	B4	BLUE data signal	I	
18	B3	BLUE data signal	I	
19	B2	BLUE data signal	I	
20	B1	BLUE data signal	I	
21	B0	BLUE data signal (LSB)	I	
22	GND	GND	-	
23	G5	GREEN data signal (MSB)	I	
24	G4	GREEN data signal	I	
25	G3	GREEN data signal	I	
26	G2	GREEN data signal	I	
27	G1	GREEN data signal	I	
28	G0	GREEN data signal (LSB)	I	
29	GND	GND	-	
30	R5	RED data signal (MSB)	I	
31	R4	RED data signal	I	
32	R3	RED data signal	I	
33	R2	RED data signal	I	
34	R1	RED data signal	I	
35	R0	RED data signal (LSB)	I	
36	GND	GND	-	
37	V _{SYNC}	Vertical synchronous signal (negative)	I	
38	H _{SYNC}	Horizontal synchronous signal (negative)	I	
39	CK	Clock signal	I	
40	GND	GND	-	

LCD connector (CN1) : 04 6240 040 023 846+ (KYOCERA)

Recommended matching FFC or FPC : 0.5mm pitch

- 1) The horizontal display start timing is settled in accordance with a rising timing of ENAB signal.
In case ENAB is fixed "Low", the horizontal start timing is determined.
Don't keep ENAB "High" during operation.
- 2)



R/L = L
U/D = H



R/L = H
U/D = H



R/L = L
U/D = L



R/L = H
U/D = L

8. Input timing characteristics

8-1. Timing characteristics

Item		Symbol	Min	Typ	Max	Unit	Note
Clock signal (CK)	Frequency	1/Tc	22.66	25.18	27.69	MHz	1) 2)
	Duty ratio	Tch/Tc	40	50	60	%	
Data signal (R0-R5, G0-G5, B0-B5)	Set up time	Tds	5	—	—	ns	
	Hold time	Tdh	10	—	—	ns	
Horizontal sync. signal (HSYNC)	Cycle	TH	30.0	31.8	—	μ s	3)
			770	800	850	clock	
	Pulse width	THp	2	96	200	clock	
Vertical sync. signal (VSYNC)	Cycle	TV	515	525	560	line	
	Pulse width	TVp	2	—	34	line	
Horizontal display period		THd	640			clock	
HSYNC-Clock phase difference		THc	10	—	Tc-10	ns	
HSYNC-VSYNC phase difference		TVh	2Tc	—	TH-THp-1	ns	
Vertical sync. signal start position		TVs	34			line	
Vertical display period		TVd	480			line	

- 1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.
Please use the display under the conditions written in the specification.
- 2) In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.
- 3) CK count of each Horizontal Scanning Time should be always the same. Vertical invalid data period should be "n" X "Horizontal Scanning Time". (n: integer) Frame period should be always the same.

8-2. Horizontal display position

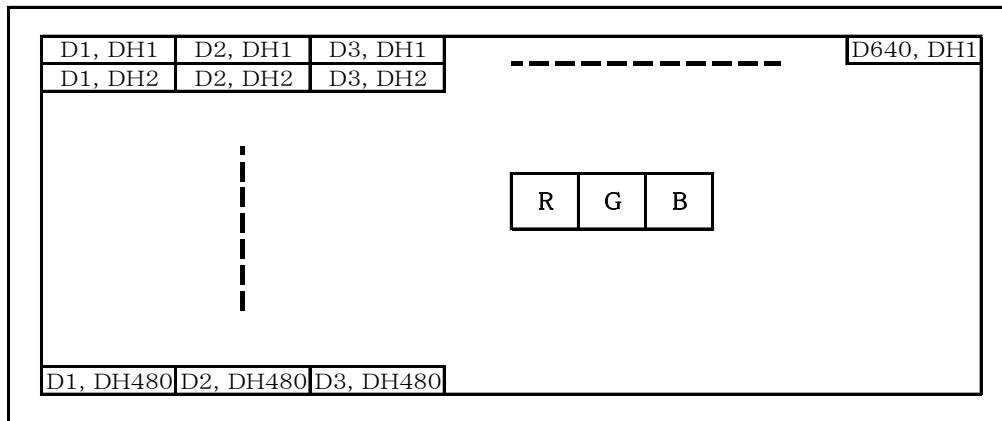
Item		Symbol	Min	Typ	Max	Unit	Note
Data Enable signal (ENAB)	Set up time	Tes	5	—	Tc-10	ns	
	Pulse width	Tep	2	640	TH-10	clock	
HSYNC-Enable signal phase difference		The	44	—	TH-664	clock	

- 1) The horizontal display position is determined by ENAB signal.
- 2) When ENAB is fixed at "Low", the display starts from the data of C104 (clock) as shown in 8-5.

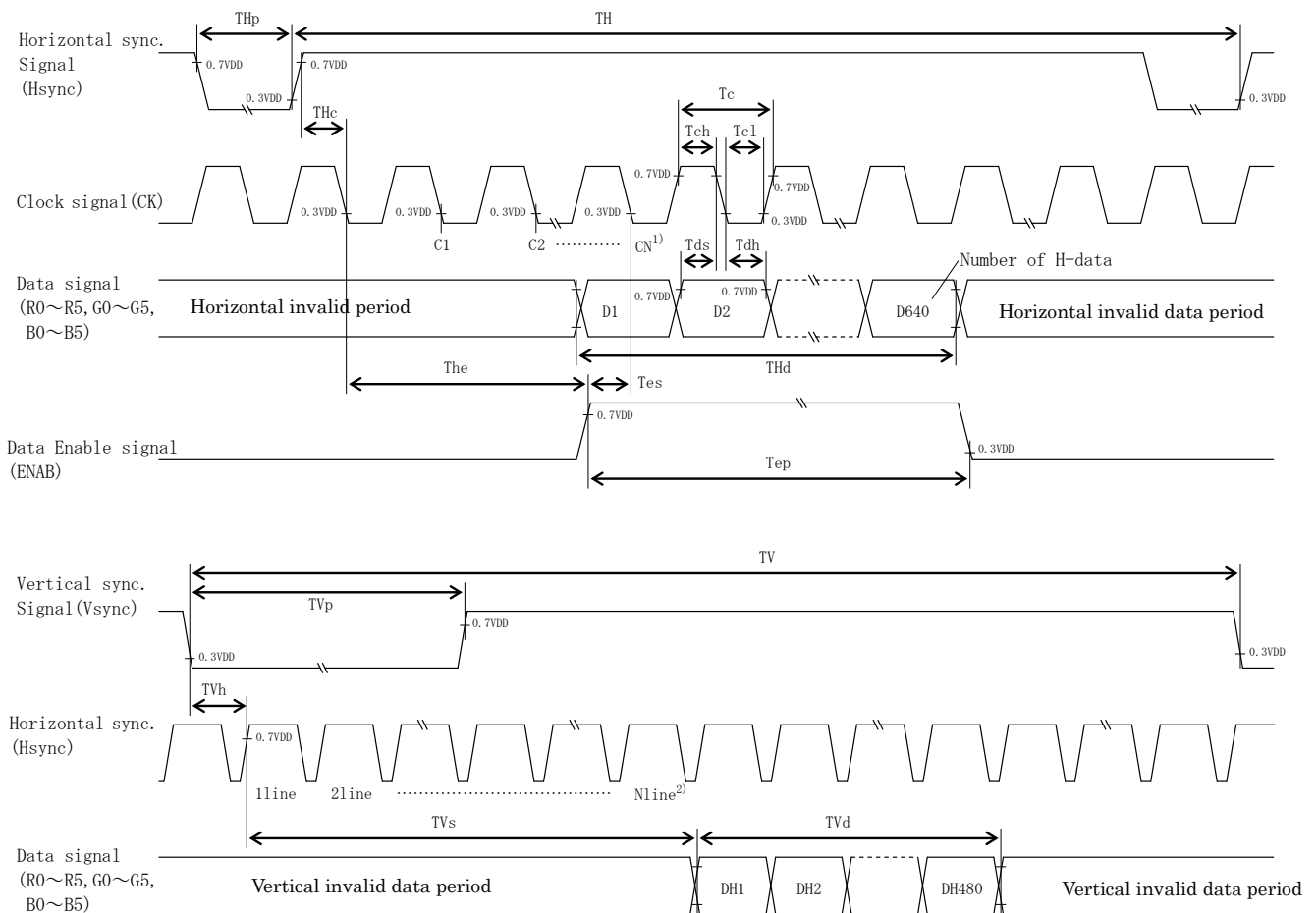
8-3. Vertical display position

- 1) The vertical display position (TVs) is 34th line.
- 2) ENAB signal is independent of vertical display position.

8-4. Input data signals and display position on the screen



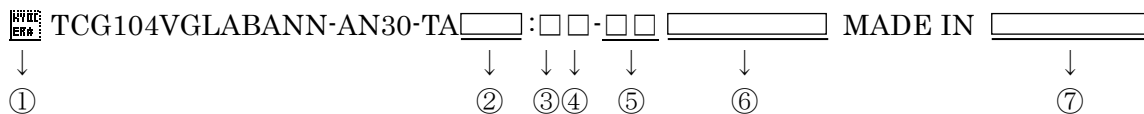
8-5. Input timing characteristics



- 1) When ENAB is fixed at "Low", the display starts from the data of C104 (Clock).
- 2) The vertical display position (TVs) is fixed at 34th line.

9. Lot number identification

The lot number shall be indicated on the back of the backlight case of each LCD.



No① – No⑦ above indicate

- ① Data matrix (For internal control purpose only)
- ② Internal reference number (Printed as necessary)
- ③ Year code (The last digit of the year)
- ④ Month code
- ⑤ Day code
- ⑥ Serial number
- ⑦ Country of origin

④ Month code

Month	Jan.	Feb.	Mar.	Apr.	May	Jun.
Code	1	2	3	4	5	6

Month	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code	7	8	9	X	Y	Z

10. Warranty

10-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

10-2. Production warranty

Kyocera warrants its LCD's for a period of 12 months from the ship date. Kyocera shall, by mutual agreement, replace or re-work defective LCD's that are shown to be Kyocera's responsibility.

11. Precautions for use

11-1. Installation of the LCD

- 1) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 2) Please ground either of the mounting (screw) holes located at each corner of an LCD, in order to stabilize brightness and display quality.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) The LCD shall be installed flat, without twisting or bending.
- 5) A transparent protection film is attached to the polarizer. Please remove the protection film slowly before use, paying attention to static electricity.

11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.
Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

11-5. Usage

- 1) **DO NOT** store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

12. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	80°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-30°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	70°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.
The reliability test is conducted only to examine the LCD's capability.

Spec No.	TQ3C-8EAF0-E2YAG91-01
Date	March 16, 2026

KYOCERA INSPECTION STANDARD

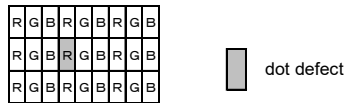
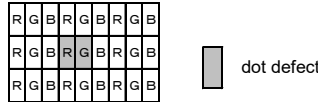
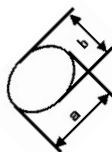
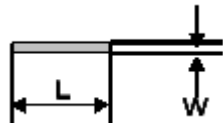
TYPE : TCG104VGLABANN-AN30-TA

KYOCERA CORPORATION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.
	Prepared	Checked	Approved	Approved
February 14, 2023	<i>N. Yamawaki</i>	<i>T. Masuda</i>	<i>I. Kawajiri</i>	<i>T. Sawada</i>

Visuals specification

1) Note

	Note		
General	1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent. 2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area. 3. Inspection conditions Luminance: 500 Lux min. Inspection distance (H1): 300 mm Distance between LCD panel surface and ND filter (H2): $0 < H2 < 30$ mm *ND filters are placed parallel to the LCD panel. Temperature: $25 \pm 5^{\circ}\text{C}$ Direction: Directly above		
Definition of inspection item	Dot defect	Bright dot defect	The dot is constantly “on” when power applied to the LCD, even when all “Black” data sent to the screen. Inspection tool: 5% Transparency neutral density filter. Count dot: If the dot is visible through the filter. Don't count dot: If the dot is not visible through the filter. 
		Black dot defect (Including circular ones)	The dot is constantly “off” when power applied to the LCD, even when all “White” data sent to the screen. Similar size compared to bright dot. Circular black dots shall be evaluated according to the criteria for circular white dots and circular black dots.
		White dot (Circular/foreign particle)	Pixel works electrically, however, circular/foreign particle makes dot appear to be “on” even when all “Black” data is sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects. 
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels “Black” or “White”) and non operating.
		Appearance inspection	Does not satisfy the value at the spec.
	Definition of size	Definition of circle size  a: major axis, b: minor axis $d = (a + b) / 2$ Definition of linear size 	

2) Standard

Classification		Inspection item		Judgement standard		
Defect (in LCD glass)	Single dot	Bright dot defect		Acceptable number : 4 Bright dot spacing : 5 mm or more		
		Black dot defect		Acceptable number : 5 Black dot spacing : 5 mm or more		
	Adjacent dot	2 dots	Bright dot defect	Acceptable number : 2		
			Black dot defect	Acceptable number : 3		
		3 or more dots		Acceptable number : 0		
	Total dot defects		Acceptable number : 5 Max			
	Others	Circular white dots and circular black dots				
				Size (mm)		Acceptable number
				d ≤ 0.2		(Neglected)
0.2 < d ≤ 0.4				5		
0.4 < d ≤ 0.5				3		
0.5 < d				0		
External inspection (Defect on Polarizer or between Polarizer and LCD glass)	Polarizer (Scratch)					
			Width (mm)		Length (mm)	Acceptable number
			W ≤ 0.1		—	(Neglected)
			0.1 < W ≤ 0.3	L ≤ 5.0		(Neglected)
				5.0 < L		0
	0.3 < W		—	0		
	Polarizer (Bubble)					
			Size (mm)		Acceptable number	
			d ≤ 0.2		(Neglected)	
			0.2 < d ≤ 0.3		5	
			0.3 < d ≤ 0.5		3	
			0.5 < d		0	
	Foreign particle (Circular shape)					
			Size (mm)		Acceptable number	
			d ≤ 0.2		(Neglected)	
			0.2 < d ≤ 0.4		5	
			0.4 < d ≤ 0.5		3	
			0.5 < d		0	
	Foreign particle (Linear shape) Scratch					
			Width (mm)		Length (mm)	Acceptable number
			W ≤ 0.03		—	(Neglected)
0.03 < W ≤ 0.1			L ≤ 2.0		(Neglected)	
			2.0 < L ≤ 4.0		3	
			4.0 < L		0	
0.1 < W			—	(According to circular shape)		
Color variation (Mura)			Not visible through 5% ND filter.			

Document No.	TQ3C-8EAF0-E3YAG91-01
Date	March 16, 2026

KYOCERA PACKAGING STANDARD

TYPE : TCG104VGLABANN-AN30-TA

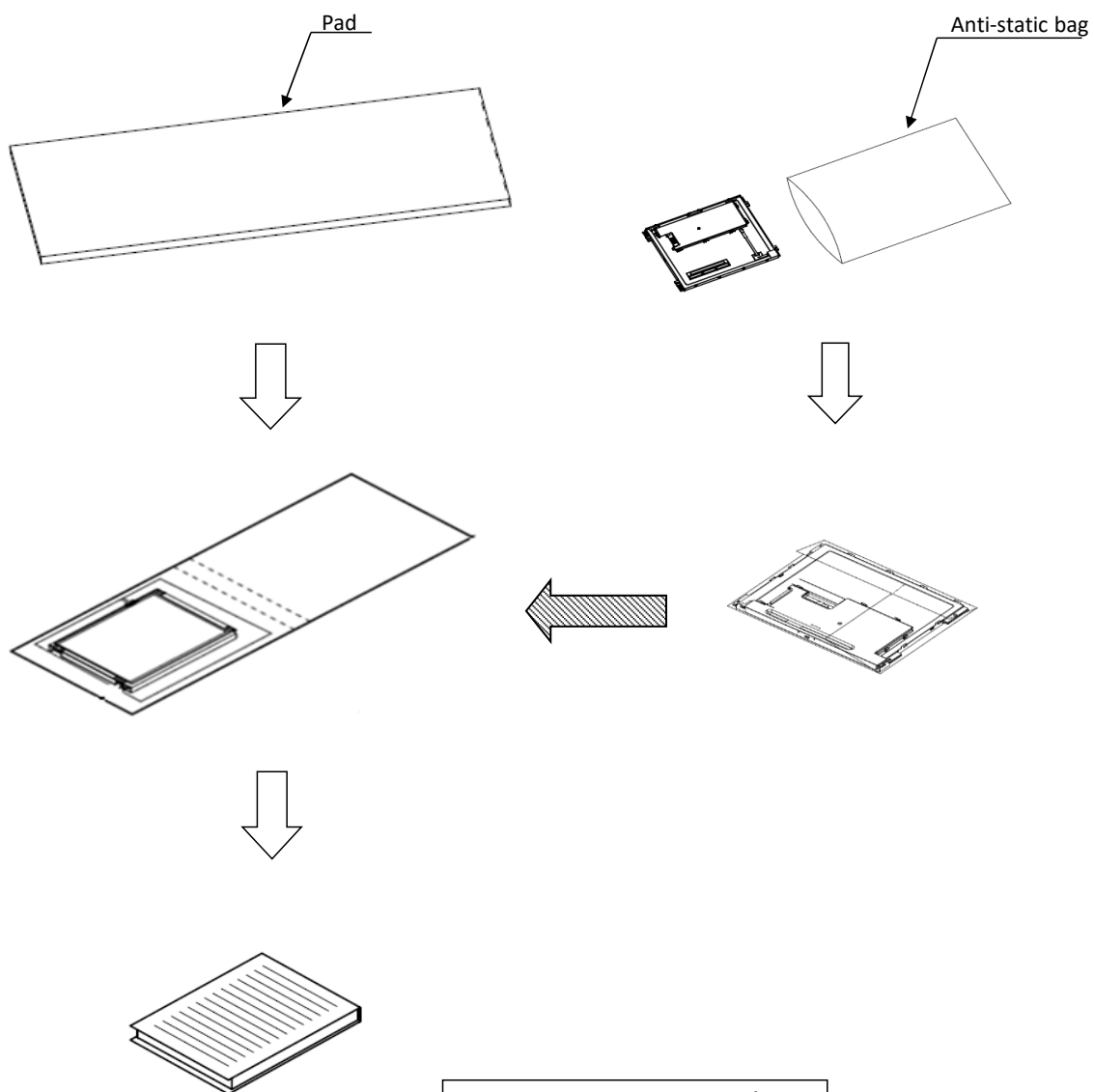
KYOCERA CORPORATION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.
	Prepared	Checked	Approved	Approved
February 14, 2023	<i>N. Yamawaki</i>	<i>T. Masuda</i>	<i>I. Kawajiri</i>	<i>T. Sawada</i>

Document No.	Part No.	Page
TQ3C-8EAF0-E3YAG91-01	TCG104VGLABANN-AN30-TA	-

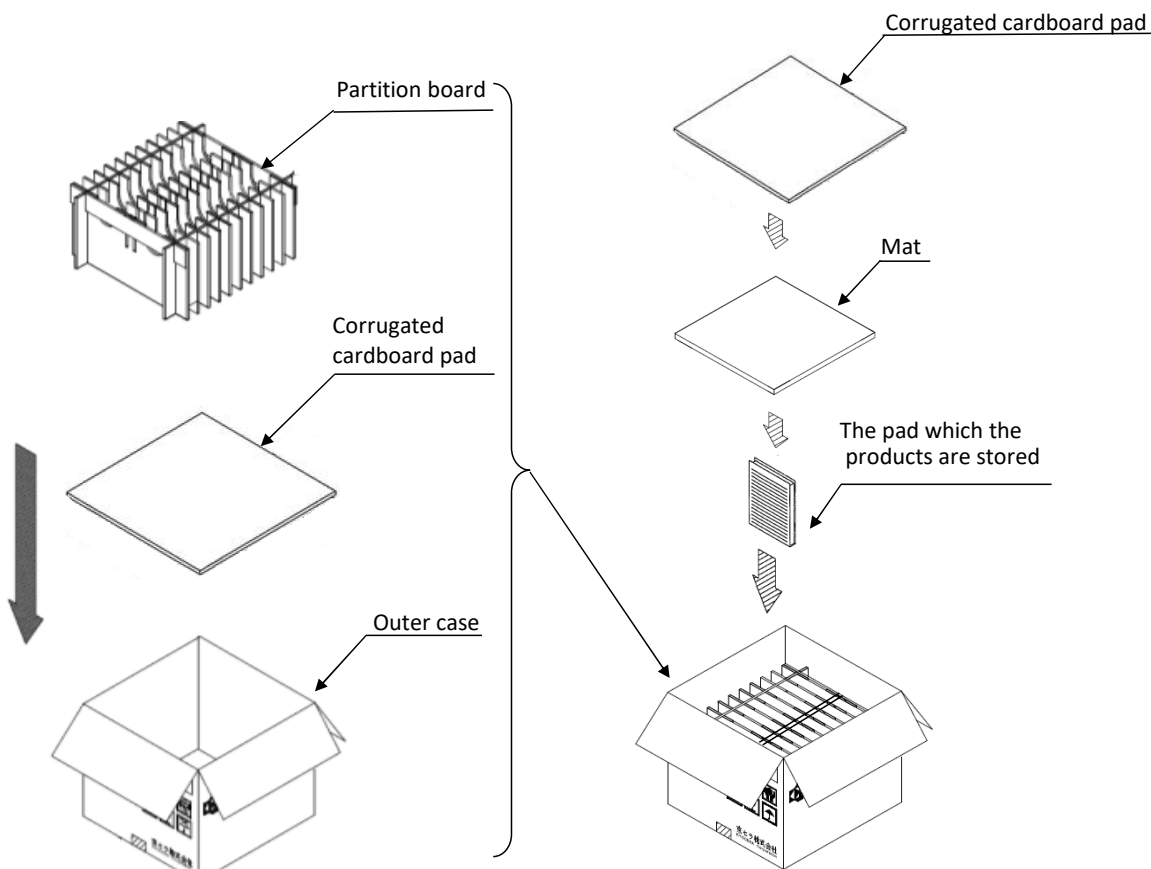
Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.
		Prepared	Checked	Approved	Approved
March 16, 2026		<i>N. Yamawaki</i>	<i>T. Masuda</i>	<i>I. Kawajiri</i>	<i>T. Sawada</i>
Rev. No.	Date	Page	Descriptions		
01	Mar. 16, 2026	-	Removed "CONFIDENTIAL" notation		

1. How to store LCDs in the pad

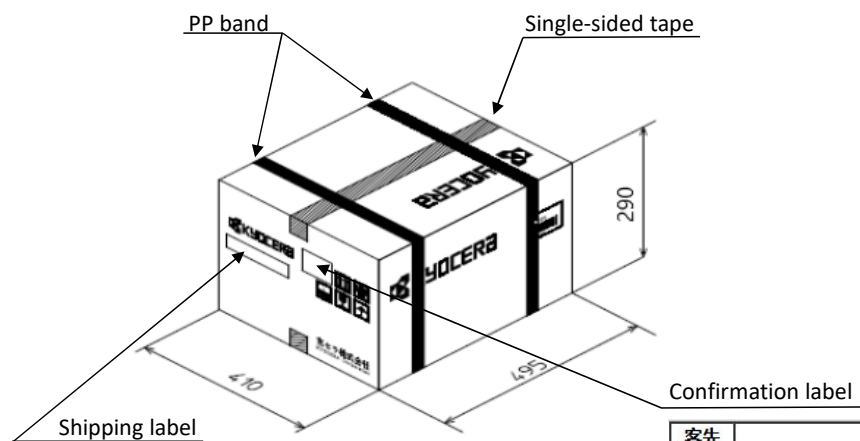
Number of storage Max.1 pc/ Pad

2. How to store LCDs in the outer case



Number of storage Max.20 pcs/ Case

3. Location of the labels



MODEL (機種番号)			
PART No. (部品注文番号)			
DESCRIPTION (品名)			
PARTS No. (GROUP No.) (部品組番号)	LOT No. (ロット番号)		
QUANTITY (数量)	DATE (日付)		
KYOCERA CORPORATION			

客先 (Customer)				京セラ株式会社
品名 (Parts No.)				
数量 (Qty)				
検査者 (Check)				
検査者 (Check)				